



IFW

PATENT  
8022-1087

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Kazuhiro IIDA

Conf. 6535

Application No. 10/523,019

Group 3764

Filed February 2, 2005

ANALYSIS CHIP AND ANALYZING APPARATUS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed Form PTO-1449.

A concise explanation of the relevance of these items is that these references were cited by the Japanese Patent Office in an Official Action. A copy of the Japanese Official Action in which they were cited is attached hereto, with what is believed to be the pertinent portion enclosed in a wavy line. An English translation of the enclosed portion is also attached hereto. JP 7-500191 was previously filed in the Information Disclosure Statement of February 2, 2005.

Under the provisions of 37 CFR 1.97(e), the undersigned hereby certifies that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign Patent Office in a

counterpart foreign application not more than three months  
prior to the filing of this Statement.

Respectfully submitted,

YOUNG & THOMPSON



---

Robert J. Patch, Reg. No. 17,355  
745 South 23<sup>rd</sup> Street  
Arlington, VA 22202  
Telephone (703) 521-2297  
Telefax (703) 685-0573  
(703) 979-4709

RJP/lk

May 7, 2007

